



6/Att.
Chenusa
9/29/02

PATENT
Customer No. 22,852
Attorney Docket No. 03180.0275-00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Eiichi HOSOMI) Group Art Unit: 2826
)
Application No.: 09/816,170) Examiner: F. Erdem
)
Filed: March 26, 2001)
)
For: SEMICONDUCTOR DEVICE)
INCLUDING MEMORY UNIT AND)
SEMICONDUCTOR MODULE)
INCLUDING MEMORY UNITS)

Commissioner for Patents
Washington, DC 20231

Sir:

AMENDMENT

In response to the Office Action dated May 22, 2002, the period for reply having been extended through September 23, 2002, (September 22nd being a Sunday) by a Petition for Extension of Time of one month and fee payment filed concurrently herewith, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend claims 3, 6, 18, and 24 as follows:

3. (AMENDED) The semiconductor device of claim 2, wherein the identifier is one bit data or a plurality of bit data, and the identifier generating circuit is an adder circuit for carrying the identifier of the memory unit bit by bit.

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